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## FN0800D083A Product Specification Rev.V0

| BUYER    |                             |
|----------|-----------------------------|
| SUPPLIER | FANNAL Electronics CO., LTD |
| FG-Code  | FN0800D083A                 |

- □ Preliminary Specification
- Approval Specification

| ITEM    | BUYER SIGNATU | RE DATE |
|---------|---------------|---------|
| Quality |               |         |
| R&D     |               |         |
| Approv  | ed            |         |
|         |               |         |

| ITEM SUP | PLIER SIGNA | TURE DATE  |
|----------|-------------|------------|
| Prepared | DONG        | 2023-05-05 |
| Reviewed | XIONG       | 2023-05-05 |
| Approved | JACK        | 2023-05-05 |
|          |             |            |

| PRODUCT GROUP    | REV | ISSUE DATE |
|------------------|-----|------------|
| TET- LCM PRODUCT | VO  | 2023-05-05 |



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## **REVISION HISTORY**

| REV. | Page. | DESCRIPTION OF CHANGES | DATE       | PREPARED |
|------|-------|------------------------|------------|----------|
| V0   |       | Initial Release        | 2023-05-05 | JACK     |
|      |       |                        |            |          |
|      |       |                        |            |          |
|      |       |                        |            |          |
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| 3.0  | Absolute Maximum Ratings                     | 6    |
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## 1.0 General Description

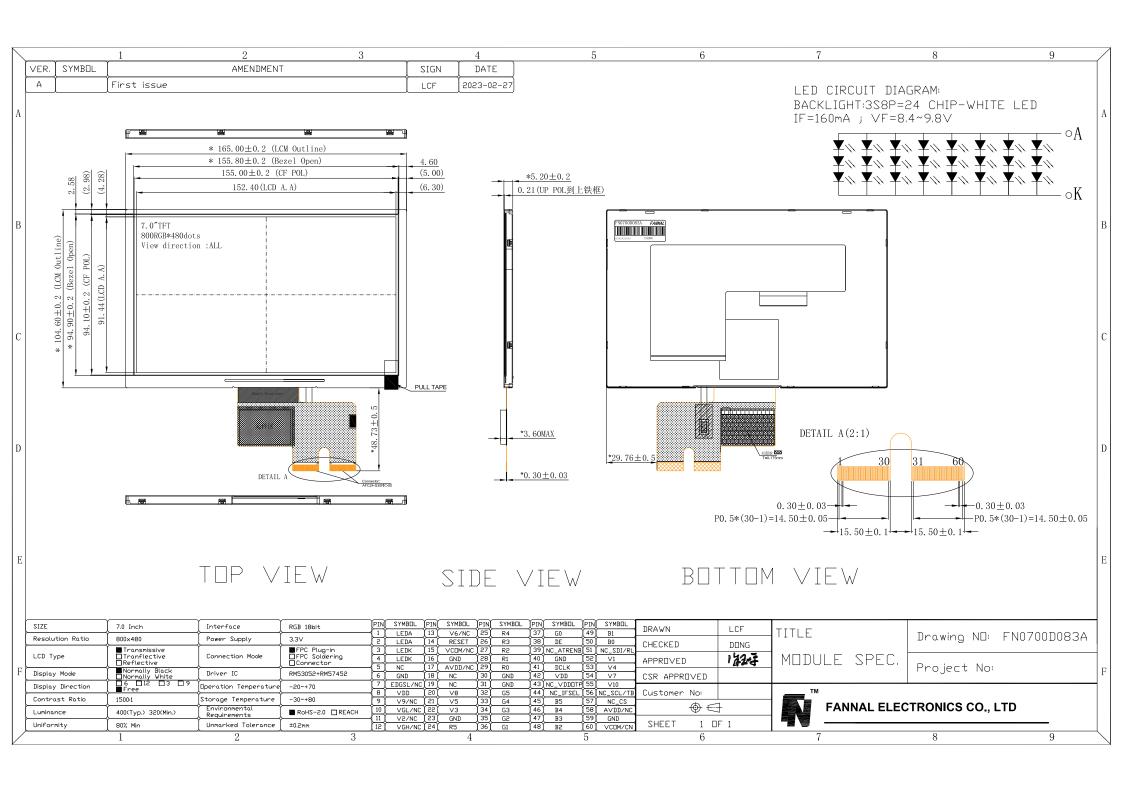
## 1.1 Application

| ✓ Industrial ✓ Medical | Outdoor highlight |
|------------------------|-------------------|
|------------------------|-------------------|

☐ Automotive ☐ Smart Home ☐ Digital & Consumer

## **1.2 General Specification**

| Parameter         | Specification            | Unit   |
|-------------------|--------------------------|--------|
| LCD size          | 7.0 (Diagonal)           | inch   |
| Resolution Ratio  | 800(H)×480(V)            | pixels |
| Pixel Pitch       | 0.13455x0.13455(V)       | mm     |
| Active Area       | 152.4(H)×91.44(V)        | mm     |
| Module Size       | 165.0(W)×104.6(H)×5.2(D) | mm     |
| Display Mode      | Normally Black           |        |
| Interface         | RGB-18bit                |        |
| Pixel arrangement | RGB-Vertical Stripe      |        |
| View Direction    | ALL                      |        |
| Power Supply      | 3.3                      | V      |
| Power Consumption | 2.0                      | W      |
| Weight            | 115                      | g      |
| Luminance         | 400 (TYP.)               | cd/m²  |
| Driver IC         | RM53052+RM57452          |        |



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## 3.0 ABSOLUTE MAXIMUM RATINGS /绝对最大额定值

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

| Parameter              | Symbol          | Min. | Max.          | Unit       |
|------------------------|-----------------|------|---------------|------------|
| Digital Supply Voltage | VDD             | -0.5 | 5.0           | V          |
| Operating Temperature  | T <sub>OP</sub> | -20  | +70           | °C         |
| Storage Temperature    | T <sub>ST</sub> | -30  | +80           | $^{\circ}$ |
| Humidity               | RH              |      | 90%(Max60 °C) | RH         |

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## 4.0 ELECTRICAL SPECIFICATIONS/电气规范

#### 4.1 TFT LCM Module

 $[Ta = 25 \pm 2 \, ^{\circ}C]$ 

| Parameter                 | Symbol | Min.    | Тур. | Max.    | Unit     |
|---------------------------|--------|---------|------|---------|----------|
| Digital Supply Voltage    | VDD    | 2.9     | 3.3  | 3.6     | V        |
| Low Level input voltage   | Vil    | 0       | -    | 0.3VDD  | <b>V</b> |
| High Level input voltage  | Vih    | 0.7VDD  | -    | VDD     | <b>V</b> |
| Low Level output voltage  | Vol    | GND     | -    | GND+0.4 | V        |
| High Level output voltage | Voh    | VDD-0.4 | -    | -       | V        |

## 4.2 Backlight Driving Conditions /背光驱动条件

 $[Ta = 25 \pm 2 \, ^{\circ}C]$ 

| Parameter       | Symbol | Min. | Тур.  | Max. | Unit | Notes  |
|-----------------|--------|------|-------|------|------|--------|
| Forward voltage | VF     | ı    | 9.0   | ı    | >    | Note 1 |
| Forward current | lF     | ı    | 160   | ı    | mA   | -      |
| LED Life Time   | -      | -    | 50000 | -    | Hrs  | Note 2 |

Note1: Under LCM operating, the stable forward current should be inputted. And forward voltage is for reference only.

Note2: Optical performance should be evaluated at Ta=25°C. if LED is driven by high current, high ambient temperature & Humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

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## **5.0 Interface Description**

| Connector Name/Designation | Interface Connector/Interface Card |
|----------------------------|------------------------------------|
| Type Part Number           | FPC 60PIN 0.5Picht                 |
| Mating Housing Part Number | FH34SRJ-30S-0.5SH*2                |

## 5.1 Pin assignment for LCM module

| Pin No. | Symbol   | Description                      |
|---------|----------|----------------------------------|
| 1-2     | LEDA     | Power for LED backlight(Anode)   |
| 3-4     | LEDK     | Power for LED backlight(Cathode) |
| 5       | NC       | No connection                    |
| 6       | GND      | Ground.                          |
| 7       | NC/EDGSL | No connection                    |
| 8       | VDD      | Power Supply (+3.3V)             |
| 9       | NC/V9    | No connection                    |
| 10      | NC/VGL   | No connection                    |
| 11      | NC/V2    | No connection                    |
| 12      | NC/VGH   | No connection                    |
| 13      | NC/V6    | No connection                    |
| 14      | RESET    | Global reset                     |
| 15      | NC/VCOM  | No connection                    |
| 16      | GND      | Ground.                          |
| 17      | NC/AVDD  | No connection                    |
| 18      | NC       | No connection                    |
| 19      | NC       | No connection                    |
| 20      | NC/V8    | No connection                    |
| 21      | NC/V5    | No connection                    |
| 22      | NC/V3    | No connection                    |
| 23      | GND      | Ground.                          |

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| Pin No. | Symbol  | Description          |
|---------|---------|----------------------|
| 24-29   | R5-R0   | Red data             |
| 30      | GND     | Ground.              |
| 31      | GND     | Ground.              |
| 32-37   | G5-G0   | Green data           |
| 38      | DE      | Data enable pin      |
| 39      | NC      | No connection        |
| 40      | GND     | Ground.              |
| 41      | DCLK    | Clock Signal Input.  |
| 42      | VDD     | Power Supply (+3.3V) |
| 43      | NC      | No connection        |
| 44      | NC      | No connection        |
| 45-50   | B5-B0   | Blue data            |
| 51      | NC      | No connection        |
| 52      | NC/V1   | No connection        |
| 53      | NC/V4   | No connection        |
| 54      | NC/V7   | No connection        |
| 55      | NC/V10  | No connection        |
| 56      | NC      | No connection        |
| 57      | NC      | No connection        |
| 58      | NC/AVDD | No connection        |
| 59      | GND     | Ground.              |
| 60      | NC/VCOM | No connection        |

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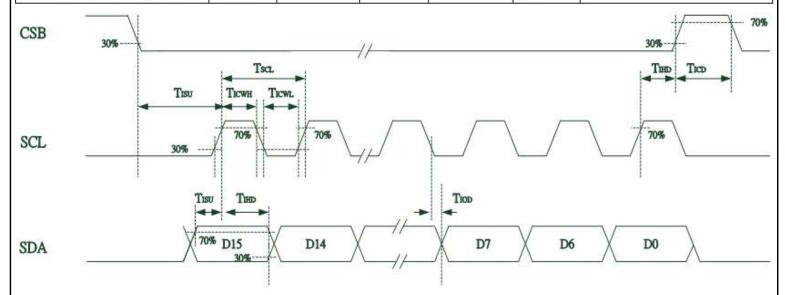


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#### **5.2 AC Electrical Characteristics**

(VDD = 2.9V to 3.6V, GND=0V, TA= -40 to +105°C)

| Parameter                  | Symbol | Min. | Тур. | Max. | Unit | Conditions         |
|----------------------------|--------|------|------|------|------|--------------------|
| Serial clock               | TSCL   | 330  |      | S #  | ns   | 8                  |
| SCL pulse high period      | TICWH  | 150  | 9    |      | ns   |                    |
| SCL pulse low period       | TICWL  | 150  |      |      | ns   |                    |
| Serial data setup time     | Tisu   | 120  |      |      | ns   |                    |
| Serial data hold time      | TIHD   | 120  | -    |      | ns   |                    |
| Serial output delay time   | TIOD   | .=   | -    | 30   | ns   | SDA at output mode |
| CSB rising to falling time | TICD   | 1    | -    |      | us   |                    |



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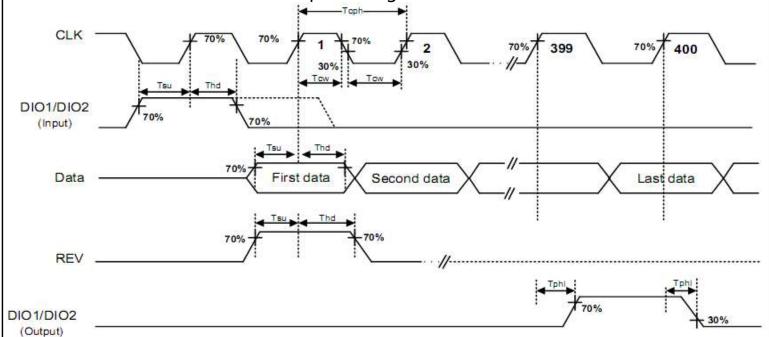


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## **5.3 Timing Diagram**

## 5.3.1 TTL mode Clock and data input timing



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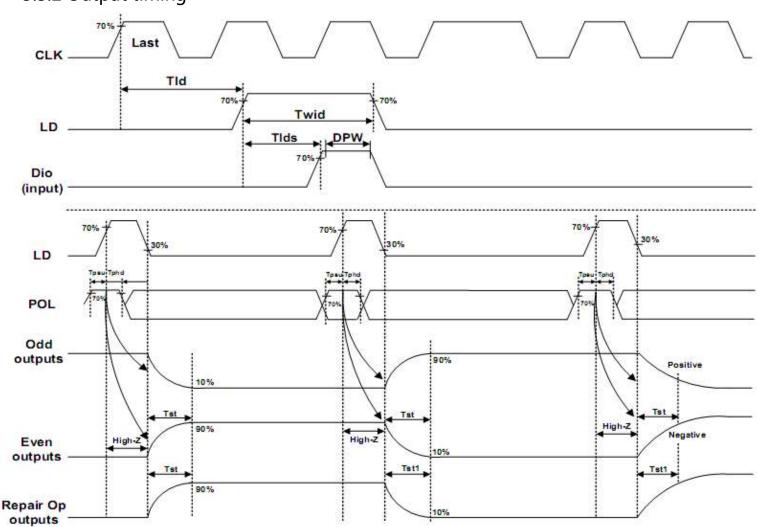
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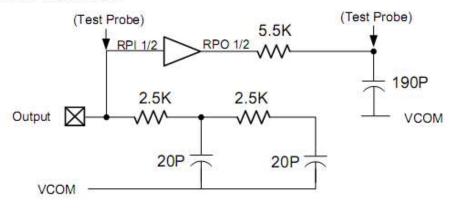
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#### 5.3.2 Output timing



#### Output load condition:

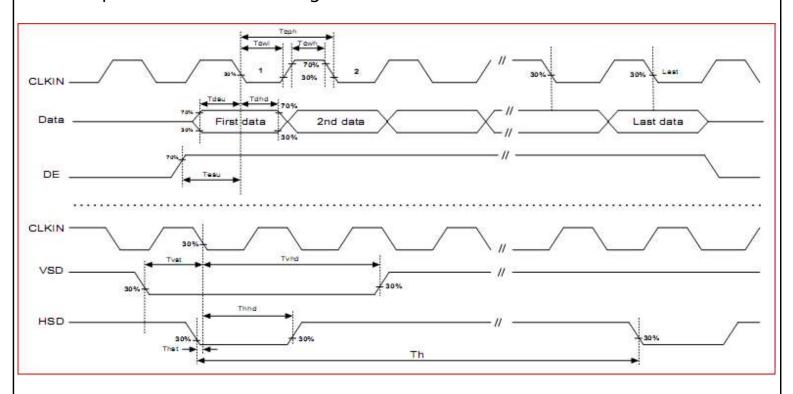


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## 5.3.3 Input Clock and data timing



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| 5.3.4 Vertical tim | ing diagram | ı HV (Casca   | ade)      |                 |        |      |         |
| VSD                |             |               |           |                 |        |      |         |
| HSD                |             |               |           |                 | Ī      |      |         |
| Data               |             | 2 3           | ]         | [N-1]           | N      |      | <u></u> |
| STV                |             |               |           |                 | -      |      |         |
| CLKV               |             |               |           |                 | _      |      |         |
| OEV                |             |               |           |                 |        |      |         |
| YV1C               |             |               | <b>——</b> |                 | İ      |      |         |
|                    |             |               |           |                 |        |      |         |
|                    |             |               |           |                 |        |      |         |

| PRODUCT G           | ROUP        | REV       | ISS       | UE DATE       |           | FANN        | ΔΙ        |  |
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| 5.3.5 Vertical timi | ing diagram | DE (Case  | cade)     |               |           |             |           |  |
|                     | Line        | Line<br>2 | Line<br>3 | Line<br>N-1   | Line<br>N |             |           |  |
| DEN                 |             | STATE OF  | E4880.00  |               |           |             |           |  |
| Data                |             | 2         | 3         | N-I           | N         |             | +         |  |
| Internal<br>VS      |             |           |           |               |           |             | Ţ         |  |
| Internal<br>HS      | j           |           |           |               | Ţ         |             | $\dagger$ |  |
| STV                 |             |           |           |               |           |             | _         |  |
| CLKV                |             |           |           | L             | _         |             |           |  |
| OEV                 |             |           | L_        |               | _         |             |           |  |
| YVIC                |             |           |           |               | T<br>T    |             |           |  |
|                     |             |           |           |               |           |             |           |  |
|                     |             |           |           |               |           |             |           |  |
|                     |             |           |           |               |           |             |           |  |

|                    |                  | ı                   |                   | T          |                  |  |
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| 5.3.6 Gate outpu   | t diagram (0     | Cascade)            |                   |            |                  |  |
|                    |                  | 9                   |                   | 10         |                  |  |
| DE                 |                  |                     |                   | <u>_</u>   |                  |  |
| LD                 |                  | -                   | Thid Line         | -          | Thid Line        |  |
|                    |                  |                     |                   |            | 2                |  |
| STV1/2             | Thstv            | •                   | Twstv             | <b>-</b> L |                  |  |
| СКУ                |                  | -                   | Twckv             | -          | Thekv            |  |
| OEV                |                  | Th                  | Twoev Twoev       | п          | noev             |  |
| -                  |                  |                     | 1.000             | Thyvle     |                  |  |
| yvic               |                  |                     |                   | Twyvie     |                  |  |
| 5.3.7 Vertical tim | ing diagram      | n HV (Dual ga       | ate)              |            |                  |  |
| -                  | r                | ļ                   |                   |            | + -              |  |
| VSD                |                  | !                   |                   |            |                  |  |
|                    |                  |                     |                   |            |                  |  |
| HSD                |                  |                     |                   |            |                  |  |
|                    |                  |                     | u u               | u          |                  |  |
| Data               | Ĩ                |                     | 1 [               |            | 1                |  |
|                    | 1                | 2 3                 | N-1               | N          |                  |  |
| STV                |                  | !<br>!              |                   |            | !<br>!<br>!      |  |
| 317                |                  |                     |                   |            | 1                |  |
|                    |                  |                     |                   |            |                  |  |
| CLKV               |                  |                     |                   |            |                  |  |
|                    |                  |                     |                   |            |                  |  |
| -                  |                  |                     | ппп               |            | <u> </u>         |  |
| OEV                |                  |                     |                   |            | 1                |  |
|                    |                  | l<br>l              |                   |            | 1<br>1<br>1<br>1 |  |
| YV1C               |                  |                     |                   |            | 1                |  |
|                    |                  |                     | U U               | ע ט ט ט    | 1                |  |

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| 5.3.8 Vertical timi |        |                    | _                 |             |            |
| DE                  | Line 1 | Line Ling 3        |                   |             |            |
| Data                | 1      | 2 3                | N-1               | N           |            |
| Internal<br>VS      |        |                    |                   | <br>        | T          |
| Internal<br>HS      |        |                    |                   |             |            |
| STV                 |        |                    |                   | i<br>!<br>! |            |
| CLKV                |        |                    |                   |             |            |
| OEV                 |        |                    |                   |             |            |
| YVIC                |        |                    |                   |             |            |
|                     |        |                    |                   |             |            |
|                     |        |                    |                   |             |            |
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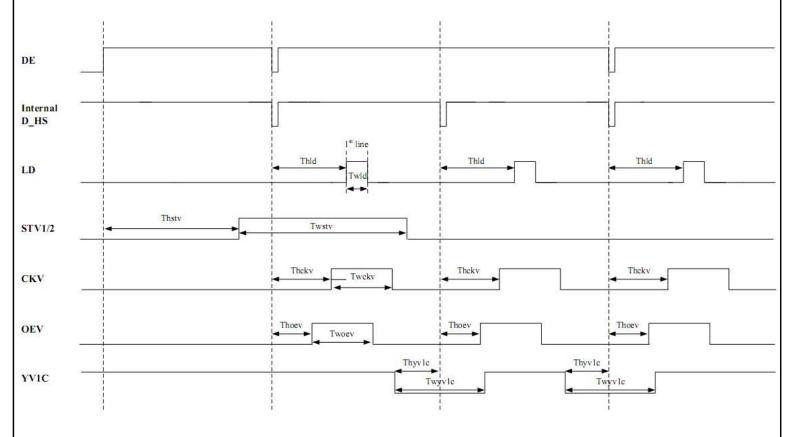
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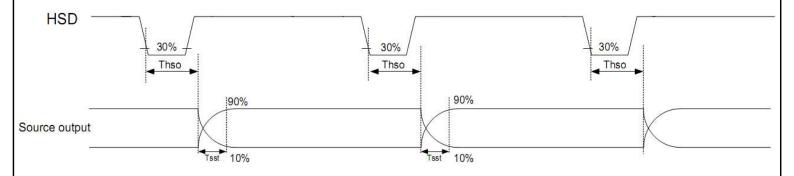
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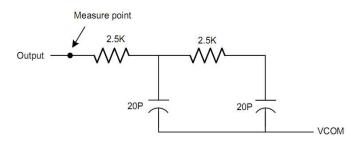
## 5.3.9 Gate output timing diagram (Dual gate)



## 5.3.10 Source output timing diagram



#### Output load condition:



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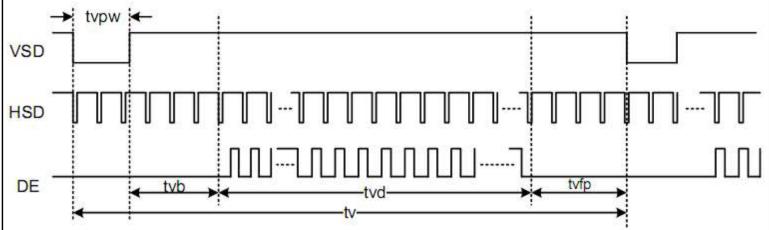


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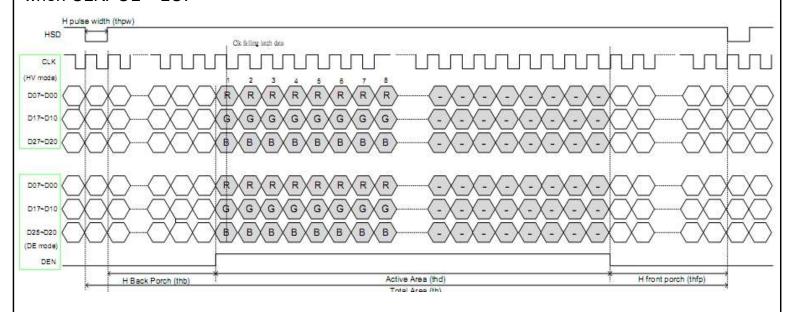
#### 5.4 Data Input Format for TTL (for IC as source driver with timing controller)

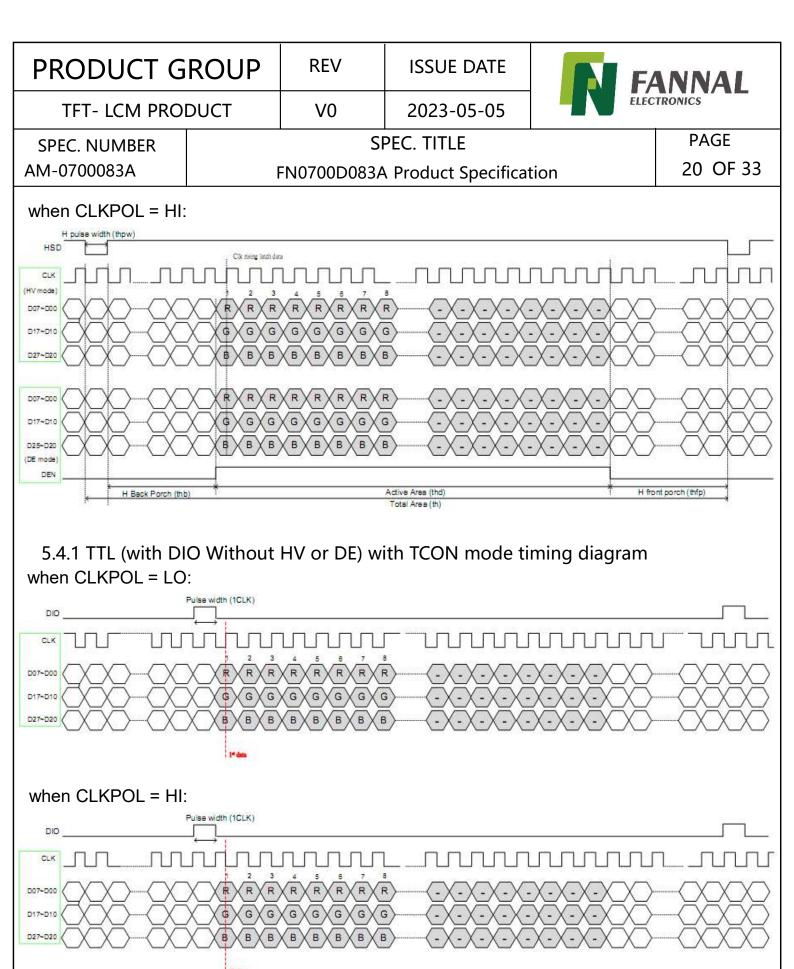
5.4.1 TTL (with HV or DE) with TCON mode timing diagram

#### Vertical input timing



## Horizontal input timing when CLKPOL = LO:





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## **5.5 Timing Characteristic**

Horizontal Input Timing Table

| Parameter                 | Symbol |      | Value |      | Unit | Note             |  |
|---------------------------|--------|------|-------|------|------|------------------|--|
|                           |        | Min. | Тур.  | Max. |      |                  |  |
| DCLK frequency            | fclk   | 20   | 33.3  | 42.8 | MHz  |                  |  |
| Horizontal display area   | thd    |      | 800   | 2    | DCLK |                  |  |
| 1 Horizontal Line         | th     | 908  | 928   | 1178 | DCLK |                  |  |
| HSD pulse width           | thpw   | 1    | 28    | 47   | DCLK | thb+thpw=48 DCLK |  |
| HSD Back Porch (Blanking) | thb    | 47   | 20    | 1    | DCLK | is fixed.        |  |
| HSD Front Porch           | thfp   | 60   | 80    | 330  | DCLK |                  |  |

#### Vertical Input Timing Table

| Parameter                    | Symbol |      | Value | Unit | Note |              |  |
|------------------------------|--------|------|-------|------|------|--------------|--|
|                              |        | Min. | Тур.  | Max. |      |              |  |
| Vertical display area        | tvd    |      | 480   | 28   | Н    |              |  |
| VSD period time              | tv     | 517  | 525   | 606  | Н    |              |  |
| VSD pulse width              | tvpw   | 1    | 1     | 3    | Н    | Tvpw+tvb=5 H |  |
| VSD Back Porch<br>(Blanking) | tvb    | 4    | 4     | 2    | Н    | is fixed     |  |
| VSD Front Porch              | tvfp   | 32   | 40    | 121  | Н    |              |  |

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## DE mode Input Timing Table

| Parameter               | Symbol     | 50    | Value | Unit  | Note |  |
|-------------------------|------------|-------|-------|-------|------|--|
|                         |            | Min.  | Тур.  | Max.  |      |  |
| DCLK frequency          | fclk       | 21.17 | 27.62 | 41.93 | MHz  |  |
| Horizontal display area | thd        |       | 800   |       | DCLK |  |
| 1 Horizontal Line       | th         | 864   | 872   | 1120  | DCLK |  |
| Horizontal blanking     | Thb + Thfp | 64    | 72    | 320   | DCLK |  |

| Parameter             | Symbol Value |      |      |      | Unit | Note |
|-----------------------|--------------|------|------|------|------|------|
|                       |              | Min. | Тур. | Max. |      |      |
| Vertical display area | tvd          |      | 480  | cs.  | Н    |      |
| Vertical period time  | tv           | 490  | 528  | 576  | Н    |      |
| Vertical Blanking     | Tvb          | 10   | 48   | 96   | Н    |      |
| Frame rate            |              | 50   | 60   | 65   | Hz   |      |

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## 6.0 OPTICAL SPECIFICATIONS /光学规格

#### 6.1 Overview /概述

The test of optical specifications shall be measured in a dark room (ambient luminance  $\leq 1$ lux and temperature =  $25\pm 2^{\circ}$ C) with the equipment of Luminance meter system (Goniom eter system and TOPCON BM-5) and test unit shall be located at an approximate distance 5 0cm from the LCD surface at a viewing angle of  $\theta$  and  $\Phi$  equal to  $\Phi$ 0°. We refer to  $\Phi$ 0=0 (= $\Phi$ 3) as the 3 o'clock direction (the "right"),  $\Phi$ 0=90 (= $\Phi$ 12) as the 12 o'clock direction ("u pward"),  $\Phi$ 0=180 (= $\Phi$ 9) as the 9 o'clock direction ("left") and  $\Phi$ 0=270(= $\Phi$ 6) as the 6 o'clock direction ("bottom"). While scanning  $\Phi$  and/or  $\Phi$ 0, the center of the measuring spot on the display surface shall stay fixed.

#### 6.2 Optical Specifications /光学规格

| Item                | Symbol       | Condition | Min   | Тур.  | Max   | Unit  | Note           |
|---------------------|--------------|-----------|-------|-------|-------|-------|----------------|
|                     | θL           |           | 80    | 85    | -     |       |                |
| Viowing Angle       | $\theta_{R}$ | Cr≥10     | 80    | 85    | ı     | dog   | Note 1         |
| Viewing Angle       | Ψτ           | C1210     | 80    | 85    | ı     | deg   | <u>ivote i</u> |
|                     | Ψв           |           | 80    | 85    | ı     |       |                |
| Contrast Ratio      | Cr           | θ=0°      | 1000  | 1500  |       | -     | Note 2         |
| Response Time       | Tr+Tf        | FF=0°     |       | 20    | 30    | ms    | Note 3         |
|                     | Wx           | 0.00      | 0.277 | 0.307 | 0.337 |       | Note 4         |
|                     | Wy           |           | 0.305 | 0.335 | 0.365 |       |                |
|                     | Rx           |           | 0.612 | 0.642 | 0.672 |       |                |
| Color Coordinate of | Ry           |           | 0.308 | 0.338 | 0.368 |       |                |
| CIE1931             | Gx           | θ=0°      | 0.258 | 0.288 | 0.318 |       |                |
|                     | Gy           |           | 0.600 | 0.630 | 0.660 |       |                |
|                     | Bx           |           | 0.131 | 0.151 | 0.181 |       |                |
|                     | Ву           |           | 0.053 | 0.083 | 0.113 |       |                |
| Uniformity          | U            |           | 80    |       |       | %     | Note 5         |
| Color Gamu          | Color Gamut  |           | 65    | 70    |       | %     |                |
| Luminance           | L            |           | 320   | 400   |       | cd/m² | Note 6         |

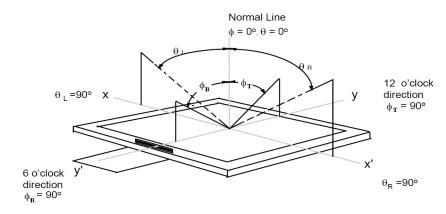
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#### Note 1:The definition of Viewing Angle

Refer to the graph below marked by  $\theta$  and  $\phi$ .



#### Note2:ThedefinitionofContrastRatio

(Contrast Ratio is measured in optimum common electrode voltage)

#### Note3:DefinitionofResponse time. (Test LCD using RD80S or similar equipments):

The output sign also photo detector are measured when the input sign also are changed from "black" to "white" (Voltage falling time) and from "white" to "black" (Voltage rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to fi gures below.

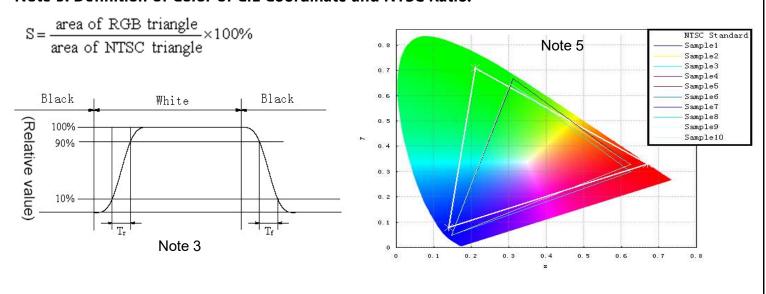
#### **Note 4: Color Coordinates of CIE 1931**

The test condition is at ILED=20mA and measured on the surface of LCD module at 25°C.

Measurement equipment: CS2000 or similar equipments

The Color Coordinate (CIE 1931) is the measurement of the center of the display shown in below figure.

#### Note 5: Definition of Color of CIE Coordinate and NTSC Ratio.



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## 7.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

| No | Test Items                                       | Conditions  | Testing standard  |
|----|--|---|---|
| 1  | High temperature storage Test                    | Ta=+80°C, 240 hours   | IEC60068-2-1:2007<br>GB2423.2-2008  |
| 2  | Low temperature storage Test                     | Ta=-30°C, 240 hours   | IEC60068-2-1:2007<br>GB2423.1-2008  |
| 3  | High temperature operation Test                  | Ta=+70°C, 240 hours   | IEC60068-2-1:2007<br>GB2423.2-2008  |
| 4  | Low temperature operation Test                   | Ta=-20°C, 240 hours   | IEC60068-2-1:2007<br>GB2423.1-2008  |
| 5  | High temperature<br>& humidity<br>(storage Test) | Ta=+60°C, 90%RH max, 240 hours  | IEC60068-2-78:2001<br>GB/T2423.3-2006   |
| 6  | Thermal shock Test                               | -30°C 30min~80°C 30min,<br>Change time:5min 20cycle   | Start with cold temperature<br>End with high temperature<br>IEC60068-2-14:1984,GB242<br>3.22-2002 |
| 7  | Vibration Test                                   | Frequency range:10Hz-55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z (6 hours for total)                 | IEC60068-2-6<br>GB/T17626.2   |
| 8  | Mechanical shock                                 | Half Sine Wave<br>100G 6ms,+X,+Y,+Z<br>3times for each direction  | IEC60068-2-27<br>GB/T2423.5   |
| 9  | Dropping Test                                    | Height: 60 cm,<br>1 corner, 3 edges, 6 surfaces   | IEC60068-2-32:1990<br>GB/T2423.8-1995   |
| 10 | ESD Test   | C=150pF, R=330 $\Omega$ , 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5times; (Environment:15°C~35°C, 30%~60%RH,86Kp a~106Kpa) | IEC61000-4-2:2001<br>GB/T17626.2-2006<br>Class C  |

#### Notes:

- Maximum acceleration 20g, 1g=9.8m/s²
   Maximum amplitude 5mm
   Maximum acceleration =0.002 x F² (frequency Hz) x D (amplitude p-pmm)

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#### 8.0 Precautions

Please pay attention to the followings when you use this TFT LCD Panel.

#### 8.1 Mounting Precautions

- (1) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (2) You must mount a module using specified mounting holes (Details refer to the drawings).
- (3) Please make sure to avoid external forces applied to the Source PCB or FPC and D-IC during the process of handling or assembling. If not, It causes panel damage or malfunction.
- (4) Note that polarizers are very fragile and could be easily damaged. Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
- (5) Do not pull or fold the source D-IC which connect the source PCB or FPC and the panel.
- Do not pull or fold the LED wire.
- (6) After removing the protective film, when the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with alcohol or purified water.
- Do not strong polar solvent because they cause chemical damage to the polarizer.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (9) Since the LCD is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it f alls from a high place or receives a strong shock, the glass may be broken.
- (10) Do not disassemble the module.
- (11) To determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- (12) If the customer's set presses the main parts of the LCD, the LCD may show the abnormal display. But this phenomenon does not mean the malfunction of the LCD and should be pressed by the way of mutual agreement.
- (13)Do not drop water or any chemicals onto the LCD's surface.

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#### **8.2 Operating Precautions**

- (1) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (2) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (3) The electrochemical reaction caused by DC voltage will lead to LCD degradation, so DC drive should be avoided.
- (4) The LCD modules use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.
- (5) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- (6) Design the length of cable to connect between the connector for back-light and the converter as short as possible and the shorter cable shall be connected directly.

The longer cable between that of back-light and that of converter may cause the luminance of LED to lower and need a higher startup voltage(Vs).

- (7) Connectors are precise devices for connecting PCB and transmitting electrical signals. Operators should insert and unplug MDL in parallel when assembling MDL.
- (8) Do not connect or disconnect the cable to/ from the module at the "Power On" condition.
- (9) When the module is operating, do not lose CLK, ENAB signals. If any one these signals is lost, the LCD panel would be damaged.
- (10) Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (11) Do not re-adjust variable resistor or switch etc.
- (12) For the Q/Single/OC Product, If the LED designed side view, LED bar should be putted in the L ong/short side; Otherwise, its reliability and function may not be guaranteed.

#### 注:

- ①(1)涉及到Pol相关条目适用于OC/MDL出货产品,
- ②(6)(7)涉及到connector相关适用于OC/MDL出货产品
- ③ (12) 涉及到客户进行BLU设计, LED Bar位置需要避开GOA位置;

#### 8.3 Electrostatic Discharge Control

- (1) Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly. Keep products as far away from static electricity as possible.
- (2) Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

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#### 8.4 Precautions for Strong Light Exposure

It is not allowed to store or run directly in strong light or in high temperature and humidity for a long ti me; Strong light exposure causes degradation of polarizer and color filter.

#### 8.5 Storage Precautions

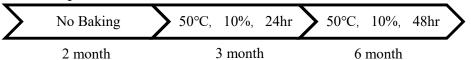
When storing modules as spares for a long time, the following precautions are necessary.

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- •(1) The polarizer surface should not come in contact with any other object.
  - It is recommended that they be stored in the container in which they were shipped.

Temperature :  $5 \sim 40$  °C

- •(2) Humidity: 35 ~ 75 %RH
- •(3) Period : 6 months
- •(4) Control of ventilation and temperature is necessary.
- •(5) Please make sure to protect the product from strong light exposure, water or moisture. Be careful for condensation.
- •(6) Store in a polyethylene bag with sealed so as not to enter fresh air outside in it.
- •(7)Do not store the LCD near organic solvents or corrosive gasses.
- •(8) Please keep the Modules/OC/FOG at a circumstance shown below Fig.



#### **8.6 Precautions for Protection Film**

- (1) Remove the protective film slowly, keeping the removing direction approximate
- 30-degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- (2) In handling the LCD, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary.

#### 8.7 Appropriate Condition for Display

- •(1) Normal operating condition
  - Temperature:  $0 \sim 40^{\circ}C$
  - Operating Ambient Humidity :  $10 \sim 90~\%$
  - Display pattern: dynamic pattern (Real display)
  - Suitable operating time: under 12 hours a day.
- •(2) Special operating condition

If the product will be used in extreme conditions such as high temperature, humidity, display patterns or 7\*24hrs operation time etc.., It is strongly recommended to contact us for Application engineering advice. Otherwise, its reliability and function may not be guaranteed.

•(3)Black image or moving image is strongly recommended as a screen save.

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- (4) Lifetime in this spec. is guaranteed only when commercial display is used according to operating usages.
- (5) Please contact us in advance when you display the same pattern for a long time.
- (6) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" or "turn off" to the screen. To avoid image sticking, it is recommended to use a screen saver.
- (7) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module m ay be damaged.
- (8) Dew drop atmosphere should be avoided.
- (9) The storage room should be equipped with a good ventilation facility and avoid to expose to corr osive gas, which has a temperature controlling system.
- (10) The LCD should be avoided to expose to corrosive gas for long time, ,the LCD may be affected by the gas as SO2 ,H2S etc.
- (11) When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD's surface which may affect the operation of the polarizer and the LCD.
- (12) Response time will be extremely delayed at lower temperature than the operating temperature r ange and on the other hand at higher temperature LCD may turn black at temperature above its opera tional range. However those phenomena do not mean malfunction or out of order with the LCD. The LCD will revert to normal operation once the temperature returns to the recommended temperature r ange for normal operation

#### 8.8 Others

#### A. LC Leak

- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it.
- In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.

#### B. Rework

- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.
- C. In order to prevent potential problems, flicker should be adjusted by optimizing the Vcom value in customer LCM Line (适用于Q/Single/OC出货产品)

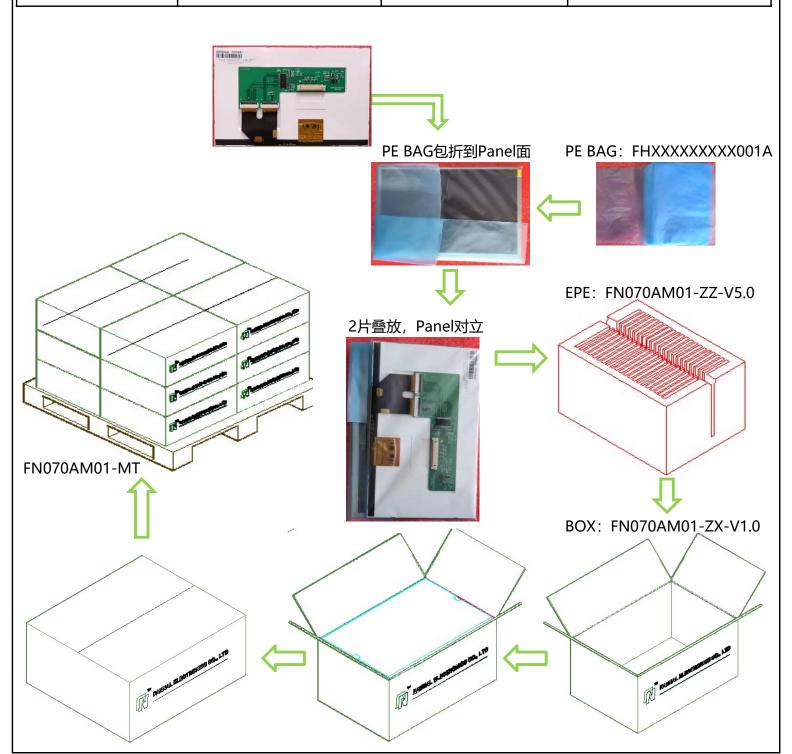
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## 9.0 PACKING INFORMATION

| LCM MODEL   | LCM Qty. in the Box | Carton Size(mm) | LCM Qty. in the Pallet |
|-------------|---------------------|-----------------|------------------------|
| FN0700D083A | 80pcs/BOX           | 530*360*275mm   | 1600pcs/Pallet         |



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#### 10.0 VISUAL INSPECTION CRITERIA FOR ALL CUSTMERS

#### 10.1 Sampling Method

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Unless otherwise agreed upon in writing, the sampling insepction shall be applied to the Customers incoming inspection.

10.1.1 Lot size: 1 pallet per same model

10.1.2 Sampling type: Random sampling

10.1.3 Inspection level: II

10.1.4 Sampling table : MIL-STD-105E

#### **10.2 Inspection Environment**

10.2.1 Ambient conditions

a. Ambient Temperature:25±3°C

b. Relative Humidity:65±20%RH

c. Ambient Illumination:300-700LUX(Normal:500LUX)



The distance between the LCM and the inspector's eyes shall be at least 30cm-50cm

#### 10.2.3 Viewing Angle

performing in front of the panel

[Vertical] :  $\pm 25$ degree [Horizontal] :  $\pm 40$ degree

#### 10.2.4 Inspection Area:

Display Area(Active Area)

#### 10.3 Definitions

#### 10.3.1 Dark / Bright Spots

Points on display which appear dark/bright and usually result form the contamination.

These defects do not vary in size or intensity(contrast)when contrast is varied.

10.3.2 Dark / Bright Lines

Lines on display which appear dark/bright and usually result from the contamination.

10.3.3 Polarizer Scratch

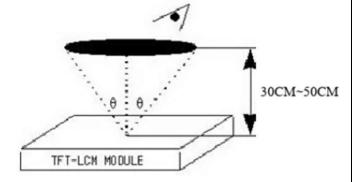
Lines on display which are seen across a darker background and do not vary in size.

10.3.4 Polarizer Dent

White spots on display which appear againse a darker backgound and do not vary in size.

#### 103.5 Bright Dot Defects

Dots(sub-pixels)on display which appear bright in the display area and visible through the 5%ND filter at Black Pattern.



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#### 10.3.6 Dark Dot Defects

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Dots(sub-pixels)on display which appear dark in the display area at R.G.B Color Patt ern.

#### 10.3.7 Line Defects

All line defects on display which appear brigh/dark such as vertical, horizontal, or cross lines.

10.3.8 Mura

Mura on display which appears darker/brighter against background birghtness on part s of display area.

10.3.9 BM Defects

Bright(white)Points on display which are off BM(Black Matrix).

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10.3.10 Visual Inspection

Inspection for LCM when the unit turns on.

10.3.11 Appearance Inspection

External inspection for LCM when the unit turns off.

10.3.12 Other

Defects which cannot be classified into the above defect definitions.

Note 1: Bright& Dark dots are not smaller than a sub-pixel (Dots smaller than a sub-pixel are not counted as d efect dots)

#### 10.4 Inspectin Criteria

Refer to 《TFT LCM general inspection standard》

#### 10.5 Verification

The supplier can verify the defective LCMs to segregate the responsibilities at customer's facility or can request the Customer to ship the defective LCMs to assigned place for verification

This verificatin result shall be agreed mutually buy the Customer and Supplier. This result can be corrected/changed after detail failure analysis at Supplier's facilities.

#### 10.6 Supplier Induced Defects

All of the Supplier induced defective LCMs shall be returned to the Supplier for repair or replacement.

Bfore return the defective LCMs, the Customer needs Supplier's confirmatin with RMA Number.

All of the returned LCMs shall be returned to the Customer within agreed time period.

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#### 10.7 Customer Induced Defects

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The Customer can return the custmoer induced defective LCMs to the Supplier for repair. The repair cost for Customer induced defective LCMs shall be agreed with both parties, Customer and Supplier.

#### 10.8 Warranty Period

In-warranty period is Eighteen(18)Months from manufacturing month of LCM Note:

- a. Eighteen months are composed of twelfth months in-warranty period and sixth mon ths distribution period
  - b. The manufacturing Month is on the LCMs as Supplier's serial No.

#### 10.9 Repair Warranty

Repair warranty is Twelve(12)Months from repaired month for repaired LCMs Note: a. The Label for repair will be added after repairing.

#### 10.10 Warranty avoidance

The warranty will be avoided in cases of below:

- a. When the warranty period is expired.
- b. The Customer induced defective LCMs.
- c. When the LCMs were repaired by 3rd party without Suppolier's approval.
- d. When the LCMs were treated like Disassemble and Rework by the Customer and/or Customer's representatives without Supplier's approval.

#### **10.11 Others**

If any problems arise with the LCMs supplied by supplier, the customer and supplier will coopeate and make ettorts to solve it with mutual contidence and respect